



Sunday, 19 April

9am

ED Session - Power Management I - Educational Session 1: Advanced Power Management Circuits
Azure

ED Session - Analog + Digital I - Educational Session 2: Designing Resilient AI-Era Chips: Thermals, Reliability, and RAS from Transistor to Datacenter
Cobalt

ED Session - Wireline I - Educational Session 3: Next-Generation High-Speed Interconnects: COM Modeling, Silicon Photonics and Low-Power Coherent Optics
Cyan A/B

9:15am

Circuits Insights
Ballroom D/E

12:15pm

Lunch Break (on own)

1:30pm

ED Session - Power Management II - Educational Session 1: Advanced Power Management Circuits
Azure

ED Session - Analog + Digital II - Educational Session 2: Designing Resilient AI-Era Chips: Thermals, Reliability, and RAS from Transistor to Datacenter
Cobalt

Educational Session 3: Next-Generation High-Speed Interconnects: COM Modeling, Silicon Photonics and Low-Power Coherent Optics
Cyan A/B

Circuits Insights
Ballroom D/E

5pm

OPEN TO ALL ATTENDEES - SSCS WIC Bingo Networking Night
Ballroom C

Monday, 20 April

8:30am

Session 1: Welcome and Opening Remarks
Ballroom A/B/C

8:50am

Session 1: Keynote Session
Ballroom A/B/C

9:40am

Break
Ballroom Foyer

10:05am

Systems and Security Forum - Session 2: Forum: Security, Privacy and Reliability for Edge Systems
Ballroom D/E

Emerging Technology I - Session 3: Optical Sensing and Filtering
Ballroom A/B/C

Data Converters I - Session 4: Data Converter Techniques for High-Speed and AI Applications
Cyan A/B

Analog Circuits and Techniques I - Session 5: Frequency Reference
Cobalt

Wireless Transceivers and RF/mm-Wave Circuits and Systems I - Session 6: Efficient and Scalable mmWave-to-Sub-THz Phased-Array ICs: From Scalability to Automation
Azure

11:45am

Lunch Break (on own)

1:30pm

Systems I - Session 7: Security Primitives and Biomedical Systems
Ballroom D/E

Emerging Technology Panel - Session 8: Panel: Integrated Photonics: The next big thing since 25 years
Ballroom A/B

Analog Circuits and Techniques Forum - Session 10: Forum: Circuits and Systems for Robotic Sensing and Control
Cobalt

Wireless Transceivers and RF/mm-Wave Circuits and Systems II - Session 11: Next-Generation RF/mmWave Building Blocks for Broadband and Efficient Transceivers
Azure

Wireline and Optical Communications Circuits and Systems I - Session 12: High-speed Wireline
Ballroom C

3:10pm

Break
Ballroom Foyer

3:35pm

Systems and Security I cont'd - Session 7: Security Primitives and Biomedical Systems
Ballroom D/E

Digital Circuits and SoCs Panel - Session 9: Panel: GPU vs PIM
Ballroom A/B

Wireless Transceivers and RF/mm-Wave Circuits and Systems II cont'd - Session 11: Next-Generation RF/mmWave Building Blocks for Broadband and Efficient Transceivers
Azure

Wireline and Optical Communications Circuits and Systems I cont'd - Session 12: High-speed Wireline
Ballroom C



Continued from **Monday, 20 April**

5:15pm
 Welcome Reception, CICC Best Paper Candidate Poster and LASCAS Poster Session
 Ballroom Foyer

Tuesday, 21 April

8am

Data Converters II - Session 13: Advanced Pipelined and SAR ADCs
 Ballroom A/B

Biomedical Technologies and Applications I - Session 14: Circuits and System Architectures for Next-Generation Neural Interfaces
 Ballroom D/E

Digital Circuits and SoCs I - Session 15: Machine Learning Accelerator
 Cyan A/B

Power Management I - Session 16: Compute Power
 Cobalt

Wireless Transceivers and RF/mm-Wave Circuits and Systems III - Session 17: Advanced Low-Power Transceiver and Clocking Techniques
 Azure

9:40am

Break
 Ballroom Foyer

10:05am

Data Converters II cont'd - Session 13: Advanced Pipelined and SAR ADCs
 Ballroom A/B

Biomedical Technologies and Applications I cont'd - Session 14: Circuits and System Architectures for Next-Generation Neural Interfaces
 Ballroom D/E

Digital Circuits and SoCs I cont'd - Session 15: Machine Learning Accelerator
 Cyan A/B

Power Management I cont'd - Session 16: Compute Power
 Cobalt

Wireless Transceivers and RF/mm-Wave Circuits and Systems III cont'd - Session 17: Advanced Low-Power Transceiver and Clocking Techniques
 Azure

11:45am

Keynote Luncheon - Session 18: Keynote Luncheon
 Ballroom C

1:30pm

Wireline and Optical Communications Circuits and Systems II - Session 19: High-performance Optical Transceivers and Die-to-die Interface
 Ballroom A/B

Emerging Technology Forum - Session 20: Forum: Heterogenous III-V Integration and Packaging
 Ballroom D/E

Digital Circuits and SoCs II - Session 22: Processors for Sensing, Media and AI
 Cyan A/B

Power Management II - Session 23: Application Specific Power
 Cobalt

Analog Circuits and Techniques II - Session 24: Sensor Interface
 Azure

3:10pm

Break
 Ballroom Foyer

3:35pm

Wireline and Optical Communications Circuits and Systems II cont'd - Session 19: High-performance Optical Transceivers and Die-to-die Interface
 Ballroom A/B

Emerging Technology II - Session 21: Circuits for Emerging Applications
 Ballroom D/E

Digital Circuits and SoCs II cont'd - Session 22: Processors for Sensing, Media and AI
 Cyan A/B

Power Management II cont'd - Session 23: Application Specific Power
 Cobalt

Analog Circuits and Techniques III - Session 25: References and (Temperature) Sensors
 Azure

5:45pm

IEEE SSCS Young Professionals and Women in Circuits Mentoring Event
 Ballroom C

Wednesday, 22 April

8:30am

Session 26: Welcome
 Ballroom A/B/C

8:50am

Session 26: Keynote Session
 Ballroom A/B/C

9:40am

Break
 Ballroom Foyer



Continued from Wednesday, 22 April	
10:05am	
Analog Circuits and Techniques IV - Session 27: Amplifiers and Track and Hold Circuits Cyan A/B	
Power Management III - Session 28: High Voltage Power Cobalt	
Wireless Transceivers and RF/mm-Wave Circuits and Systems IV - Session 29: High Performance VCOs Azure	
Systems and Security II - Session 30: HPC Chiplet Systems (CICC/CHISIC) Ballroom D/E	
11:45am	
Lunch (on own)	
1:30pm	
Data Converters III - Session 31: Noise-shaping ADCs Ballroom D/E	
Biomedical Technologies and Applications II - Session 32: Low-Power SoCs and Accelerators for Advanced Biomedical Systems Ballroom A/B	
Digital Circuits and SoCs III - Session 33: Compute in Memory for Emerging Applications Cyan A/B	

Power Management IV - Session 34: Portable, Sensing, and Wearable Power Cobalt
Wireless Transceivers and RF/mm-Wave Circuits and Systems V - Session 35: Techniques for Phase-Lock Loops and Frequency Multiplier Azure
CHISIC Tutorials Ballroom C
3:10pm
Break Ballroom Foyer
3:30pm
Break Ballroom Foyer
3:35pm
Data Converters III cont'd - Session 31: Noise-shaping ADCs Ballroom D/E
Biomedical Technologies and Applications II cont'd - Session 32: Low-Power SoCs and Accelerators for Advanced Biomedical Systems Ballroom A/B
Digital Circuits and SoCs III cont'd - Session 33: Compute in Memory for Emerging Applications Cyan A/B

Power Management IV cont'd - Session 34: Portable, Sensing, and Wearable Power Cobalt
Wireless Transceivers and RF/mm-Wave Circuits and Systems V cont'd - Session 35: Techniques for Phase-Lock Loops and Frequency Multiplier Azure
3:45pm
CHISIC Tutorials and Keynote Ballroom C
6:35pm
CHISIC Networking & Exhibits Ballroom Foyer

Thursday, 23 April	
7am	
CHISIC WORKSHOP REGISTRANTS ONLY - Breakfast (provided) Ballroom A/B	
7:45am	
Workshop on Internet of Bodies - Opening Remarks Ballroom D/E	
8am	
CHISIC Workshop Ballroom C	

Workshop on Internet of Bodies Ballroom D/E
9:50am
Break Ballroom Foyer
10am
Break and Group Pictures Ballroom Foyer
10:20am
CHISIC Workshop Ballroom C
10:30am
Workshop on Internet of Bodies Ballroom D/E
12pm
Group Pictures
12:05pm
Lunch Break (provided) & Panel: Breaking Down Chiplet Barriers: 100x Reach Optical Integration for AI-Scale Disaggregation Ballroom A/B
12:15pm
Lunch Break and Research Forum Poster Session Azure



Continued from **Thursday, 23 April**

1:05pm

CHISIC Workshop
Ballroom C

2pm

Workshop on Internet of Bodies
Ballroom D/E

2:45pm

Break
Ballroom Foyer

3pm

CHISIC Workshop
Ballroom C

3:15pm

Break
Ballroom Foyer

3:30pm

Workshop on Internet of Bodies
Ballroom D/E

3:50pm

CHISIC Closing Ceremony
Ballroom C