

Sunday, 19 April

9am

ED Session - Power Management I - Educational Session 1: Advanced Power Management Circuits
Azure

ED Session - Analog + Digital I - Educational Session 2:
Cobalt

ED Session - Wireline I - Educational Session 3: Next-Generation High-Speed Interconnects: COM Modeling, Silicon Photonics and Low-Power Coherent Optics
Cyan A/B

9:15am

Circuits Insights
Ballroom D/E

12:15pm

Lunch Break (on own)

1:30pm

ED Session - Power Management II - Educational Session 1: Advanced Power Management Circuits
Azure

ED Session - Analog + Digital II - Educational Session 2:
Cobalt

ED Session - Wireline II - Educational Session 3: Next-Generation High-Speed Interconnects: COM Modeling, Silicon Photonics and Low-Power Coherent Optics
Cyan A/B

Circuits Insights
Ballroom D/E

5pm

OPEN TO ALL ATTENDEES - SSCS WIC Bingo Networking Night
Ballroom C

Monday, 20 April

8:30am

Session 1: Welcome and Opening Remarks
Ballroom A/B/C

8:50am

Session 1: Keynote Session
Ballroom A/B/C

9:40am

Break
Ballroom Foyer

10:05am

Systems and Security Forum - Session 2: Forum: Security, Privacy and Reliability for Edge Systems
Ballroom D/E

Emerging Technology I - Session 3: Optical Sensing and Filtering
Ballroom A/B/C

Data Converters I - Session 4: Data Converter Techniques for High-Speed and AI Applications
Cyan A/B

Analog Circuits and Techniques I - Session 5: Frequency Reference
Cobalt

Wireless Transceivers and RF/mm-Wave Circuits and Systems I - Session 6: Efficient and Scalable mmWave-to-Sub-THz Phased-Array ICs: From Scalability to Automation
Azure

11:45am

Lunch Break (on own)

1:30pm

Systems I - Session 7: Security Primitives and Biomedical Systems
Ballroom D/E

Emerging Technology Panel - Session 8: Panel: Integrated Photonics: The next big thing since 25 years
Ballroom A/B

Analog Circuits and Techniques Forum - Session 10: Forum: Circuits and Systems for Robotic Sensing and Control
Cobalt

Wireless Transceivers and RF/mm-Wave Circuits and Systems II - Session 11: Next-Generation RF/mmWave Building Blocks for Broadband and Efficient Transceivers
Azure

Wireline and Optical Communications Circuits and Systems I - Session 12: High-speed Wireline
Ballroom C

3:10pm

Break
Ballroom Foyer

3:35pm

Systems and Security I cont'd - Session 7: Security Primitives and Biomedical Systems
Ballroom D/E

Digital Circuits and SoCs Panel - Session 9: Panel: GPU vs PIM
Cyan A/B

Wireless Transceivers and RF/mm-Wave Circuits and Systems II cont'd - Session 11: Next-Generation RF/mmWave Building Blocks for Broadband and Efficient Transceivers
Azure

Wireline and Optical Communications Circuits and Systems I cont'd - Session 12: High-speed Wireline
Ballroom C

<p>Continued from Monday, 20 April</p>	<p>9:40am</p>	<p>1:30pm</p>	<p>Digital Circuits and SoCs II cont'd - Session 22: Processors for Sensing, Media and AI Cyan A/B</p>
<p>5:15pm</p> <p>Welcome Reception, CICC Best Paper Candidate Poster and LASCAS Poster Session Ballroom Foyer</p>	<p>Break Ballroom Foyer</p>	<p>Wireline and Optical Communications Circuits and Systems II - Session 19: High-performance Optical Transceivers and Die-to-die Interface Ballroom A/B</p>	<p>Power Management II cont'd - Session 23: Application Specific Power Cobalt</p>
<p>Tuesday, 21 April</p>	<p>10:05am</p> <p>Data Converters II cont'd - Session 13: Advanced Pipelined and SAR ADCs Ballroom A/B</p>	<p>Emerging Technology Forum - Session 20: Forum: Heterogenous III-V Integration and Packaging Ballroom D/E</p>	<p>Analog Circuits and Techniques III - Session 25: References and (Temperature) Sensors Azure</p>
<p>8am</p> <p>Data Converters II - Session 13: Advanced Pipelined and SAR ADCs Ballroom A/B</p> <p>Biomedical Technologies and Applications I - Session 14: Circuits and System Architectures for Next-Generation Neural Interfaces Ballroom D/E</p> <p>Digital Circuits and SoCs I - Session 15: Machine Learning Accelerator Cyan A/B</p> <p>Power Management I - Session 16: Compute Power Cobalt</p> <p>Wireless Transceivers and RF/mm-Wave Circuits and Systems III - Session 17: Advanced Low-Power Transceiver and Clocking Techniques Azure</p>	<p>Biomedical Technologies and Applications I cont'd - Session 14: Circuits and System Architectures for Next-Generation Neural Interfaces Ballroom D/E</p> <p>Digital Circuits and SoCs I cont'd - Session 15: Machine Learning Accelerator Cyan A/B</p> <p>Power Management I cont'd - Session 16: Compute Power Cobalt</p> <p>Wireless Transceivers and RF/mm-Wave Circuits and Systems III cont'd - Session 17: Advanced Low-Power Transceiver and Clocking Techniques Azure</p>	<p>Digital Circuits and SoCs II - Session 22: Processors for Sensing, Media and AI Cyan A/B</p> <p>Power Management II - Session 23: Application Specific Power Cobalt</p> <p>Analog Circuits and Techniques II - Session 24: Sensor Interface Azure</p>	<p>5:45pm</p> <p>IEEE SSCS Young Professionals and Women in Circuits Mentoring Event Ballroom C</p>
	<p>11:45am</p> <p>Keynote Luncheon - Session 18: Keynote Luncheon Ballroom C</p>	<p>3:10pm</p> <p>Break Ballroom Foyer</p>	<p>Wednesday, 22 April</p> <p>8:30am</p> <p>Session 26: Welcome Ballroom A/B/C</p> <p>8:50am</p> <p>Session 26: Keynote Session Ballroom A/B/C</p> <p>9:40am</p> <p>Break Ballroom Foyer</p>

Continued from Wednesday, 22 April		Thursday, 23 April	
10:05am			9:50am
Analog Circuits and Techniques IV - Session 27: Amplifiers and Track and Hold Circuits	Cyan A/B	Power Management IV - Session 34: Portable, Sensing, and Wearable Power	Break
Power Management III - Session 28: High Voltage Power	Cobalt	Cobalt	Ballroom Foyer
Wireless Transceivers and RF/mm-Wave Circuits and Systems IV - Session 29: High Performance VCOs	Azure	Wireless Transceivers and RF/mm-Wave Circuits and Systems V - Session 35: Techniques for Phase-Lock Loops and Frequency Multiplier	9:55am
Systems and Security II - Session 30: HPC Chiplet Systems (CICC/CHISIC)	Ballroom C	Azure	Break and Group Pictures
11:45am	Lunch (on own)	3:10pm	Ballroom Foyer
1:30pm	Data Converters III - Session 31: Noise-shaping ADCs	3:30pm	CHISIC Tutorials
Biomedical Technologies and Applications II - Session 32: Low-Power SoCs and Accelerators for Advanced Biomedical Systems	Ballroom D/E	3:35pm	Ballroom C
Digital Circuits and SoCs III - Session 33: Compute in Memory for Emerging Applications	Cyan A/B	Data Converters III cont'd - Session 31: Noise-shaping ADCs	6:35pm
		Biomedical Technologies and Applications II cont'd - Session 32: Low-Power SoCs and Accelerators for Advanced Biomedical Systems	CHISIC Networking & Exhibits
		Digital Circuits and SoCs III cont'd - Session 33: Compute in Memory for Emerging Applications	
			CHISIC Tutorials and Keynote
			Ballroom C
			10:20am
			CHISIC Workshop
			Ballroom C
			Workshop on Internet of Bodies
			12pm
			Group Pictures
			12:05pm
			Lunch Break (provided) - Panel Session
			12:15pm
			Lunch Break and Student Poster Session
			1:05pm
			CHISIC Workshop
			Ballroom C
			1:30pm
			Workshop on Internet of Bodies

Continued from **Thursday, 23 April**

2:45pm

Break
Ballroom Foyer

2:50pm

Break
Ballroom Foyer

3pm

CHISIC Workshop
Ballroom C

3:15pm

Workshop on Internet of Bodies

3:50pm

CHISIC Closing Ceremony
Ballroom C