



2024 Program-at-a-Glance

Denver, Colorado, USA (Mountain Daylight Time (MDT))

Sunday, April 21, 2024

All Sunday sessions are included with conference registration

Sunday, April 21, 2024					
<i>Crystal Ballroom I</i>	<i>Crystal Ballroom II</i>	<i>Crystal Ballroom III</i>	<i>Colorado Ballroom II/III</i>		
9:00 am-4:45 pm (12:15 pm-1:30 pm break) Educational Session 1: Deep Learning and Compute-in-Memory Designs and Applications	9:00 am-4:45 pm (12:15 pm-1:30 pm break) Educational Session 2: Introduction to Quantum Computing for Circuit Designers	9:00 am-4:45 pm (12:15 pm-1:30 pm break) Educational Session 3: Optical and Wireline Communication Circuit Techniques	9:00 am-4:45 pm (12:15 pm-3:15 pm break) Educational Session 4: Nascent Sensing Devices and Interfaces		
Monday, April 22, 2024					
8:00 am-8:20 am Welcome and Opening Remarks <i>Grand Ballroom I/II</i>					
8:20 am-9:10 am Session 1: Keynote Session <i>Grand Ballroom I/II</i>					
9:10 am-9:30 am BREAK <i>Grand Ballroom Foyer</i>					
<i>Grand Ballroom I/II</i>	<i>Crystal Ballroom I</i>	<i>Crystal Ballroom II</i>	<i>Crystal Ballroom III</i>	<i>Colorado Ballroom II/III</i>	<i>Grand Ballroom IV</i>
9:30 am-11:00 am Session 2: Panel: Cognitive Connections: Exploring Brain-Computer-Interfaces through Systems and Experiments	9:30 am-11:30 am Session 3: Forum: AI-based Chip/Chiplet Generation	9:30 am-11:30 am Session 4: Forum: Cutting-edge Energy Harvesting Interface Circuits & Systems	9:30 am-11:40 am Session 5: Advances in RF/mmWave Wireless Transceivers	9:30 am-11:00 am Session 6: Panel: Will open source design be the future direction?	9:30 am-11:35am A-SSCC Best Student Papers
11:40 am-1:00 pm LUNCH BREAK (on own)					
1:00 pm-4:15 pm (2:45 pm-3:00 pm break) Session 7: Mixed-Signal Compute in Memory	1:00 pm-2:45 pm Session 8: Emerging Systems and Integration Techniques	1:00 pm-4:40 pm (2:45 pm-3:00 pm break) Session 9: Power Management Techniques	1:00 pm-4:40 pm (2:45 pm-3:00 pm break) Session 10: mmWave and THz Circuits and Systems	1:00 pm-4:15 pm (2:45 pm-3:00 pm break) Session 11: Emerging Computing Technologies and Applications	1:00 pm-2:45 pm Session 12: High-voltage and Dynamic Comparators
	3:00 pm-4:30 pm Session 8b: Panel: Can Academia Effectively Participate in Heterogeneous Integration Research and How?				3:00 pm-5:10 pm Session 13: Analog Sensor Interfaces
5:30 pm-7:30 pm Welcome Reception <i>Grand Ballroom III</i>					
7:30 pm CICC 2024 Brewery Night CICC will cover the first round of beer for the first 150 attendees @ Station 26 Brewery					



2024 Program-at-a-Glance

Denver, Colorado, USA (Mountain Daylight Time (MDT))

Tuesday, April 23, 2024

<i>Grand Ballroom I/II</i>	<i>Crystal Ballroom I</i>	<i>Crystal Ballroom II</i>	<i>Crystal Ballroom III</i>	<i>Colorado Ballroom II/III</i>
8:00 am-11:15 am (9:45 am-10:00 am break) Session 14: Domain-Specific Accelerators	8:00 am-11:15 am (9:45 am-10:00 am break) Session 15: Innovations in Sensing, Communication, and Imaging Technologies	8:00 am-11:40 am (9:45 am-10:00 am break) Session 16: Energy Harvesting and Isolated Power Conversion	8:00 am-11:15 am (9:45 am-10:00 am break) Session 17: RF/mm-Wave VCO and Phase Locked Loops	8:00 am-11:40 am (9:45 am-10:00 am break) Session 18: Power Efficient and Application-Oriented ADCs
12:00 pm-1:30 pm Session 19: Keynote Luncheon Session (Registration required) <i>Grand Ballroom III</i>				
1:45 pm-5:00 pm (3:30 pm-3:45 pm break) Session 20: Digital Circuit Techniques	1:45 pm-5:25 pm (3:30 pm-3:45 pm break) Session 21: Machine Learning and Vision Processing Systems	1:45 pm-5:25 pm (3:30 pm-3:45 pm break) Session 22: DC-DC Converters	1:45 pm-5:00 pm (3:30 pm-3:45 pm break) Session 23: Wireline Transceivers and Clocking Techniques	1:45 pm-5:25 pm (3:30 pm-3:45 pm break) Session 24: High-Resolution ADCs
4:30 pm-6:00 pm IEEE SSCS Young Professionals and Women in Circuits Mentoring Event <i>Characters Bar (next to Colorado Ballroom)</i>				
5:30 pm-7:30 pm CICC Conference Reception <i>Grand Ballroom IV</i>				
Wednesday, April 24, 2024				
8:00 am-8:50 am Session 25: Keynote Session <i>Grand Ballroom I/II</i>				
8:50 am-9:10 am BREAK				
<i>Grand Ballroom I/II</i>	<i>Crystal Ballroom I</i>	<i>Crystal Ballroom II</i>	<i>Crystal Ballroom III</i>	<i>Colorado Ballroom II/III</i>
9:10 am-11:35 am (10:30 am-10:45 am break) Session 26: Digital Compute in Memory	9:10 am-12:25 pm (10:30 am-10:45 am break) Session 27: Advanced Neural Interfaces	9:10 am-12:25 pm (10:30 am-10:45 am break) Session 28: High-Speed Data Converters	9:10 am-10:55 am Session 29: Energy-efficient Radios for IoT and Emerging Systems	9:10 am-12:25 pm (10:30 am-10:45 am break) Session 30: Optical Transceivers and Building Blocks
12:25 pm- 1:45 pm LUNCH BREAK (on own)				
1:45 pm-3:15 pm Session 31: Panel: How can LLMs help hardware design and will it replace digital design roles in the years to come?	1:45 pm-3:30 pm Session 32: Emerging Systems and Integration Techniques (Part 2)	1:45 pm-3:05 pm Session 33: Timing References	1:45 pm-3:50 pm Session 34: Forum: Wireless Transceivers Towards Next G	1:45 pm-3:50 pm Session 35: Forum: Circuits and Packaging Techniques for Next-gen Wireline Communications
3:45 pm-4:00 pm BREAK <i>Grand Ballroom Foyer</i>				
4:00 pm-5:00 pm Best Paper Poster Session & Closing/Awards Ceremony – Grand Ballroom I/II				