



## 2023 Program-at-a-Glance

Conference will take place in San Antonio, Texas, USA, Central Daylight Time (CDT)

Sunday, April 23, 2023				
<i>*All Sunday sessions are included with conference registration*</i>				
Salon A	Salon B	Salon C	Salon F	
9:00 am-4:45 pm <i>(12:15 pm-1:30 pm break)</i> <b>Educational Session 1:</b> Crystal-Less Timing/Frequency References	9:00 am-4:45 pm <i>(12:15 pm-1:30 pm break)</i> <b>Educational Session 2:</b> Emerging Devices and Systems for Storage and Computing	9:00 am-4:45 pm <i>(12:15 pm-1:30 pm break)</i> <b>Educational Session 3:</b> Wearable and Implantable Sensors	9:00 am-4:45 pm <i>(12:15 pm-1:30 pm break)</i> <b>Educational Session 4:</b> Millimeter Wave/ sub-THz Phased Array Systems	
Monday, April 24, 2023				
8:00 am-8:20 am <b>Welcome and Opening Remarks</b> <i>Salon C</i>				
8:20 am-9:10 am <b>Session 1:</b> Keynote Session <i>Salon C</i>				
<b>9:10 am-9:30 am BREAK</b>				
Salon A	Salon B	Salon C	Salon E	Salon F
9:30 am-11:40 am <b>Session 2:</b> Low-power Digital Circuits	9:30 am-11:30 am <b>Session 3:</b> Forum: Ultra High-Speed Data Converters	9:30 am-11:40 am <b>Session 4:</b> Gate Drivers and GaN ICs	9:30 am-11:40 am <b>Session 5:</b> Low Power Quantum Computing & Wireless Transceivers	9:30 am-11:40 am <b>Session 6:</b> Architectures for Advancing Computing
<b>11:40 am-1:00 pm BREAK</b>				
1:00 pm-4:40 pm <i>(2:45 pm-3:00 pm break)</i> <b>Session 7:</b> Compute in Memory and Ising Machines	1:00 pm-4:15 pm <i>(2:45 pm-3:00 pm break)</i> <b>Session 8:</b> Data Converter Design Techniques	1:00 pm-5:05 pm <i>(2:45 pm-3:00 pm break)</i> <b>Session 9:</b> DC-DC Converters	1:00 pm-4:15 pm <i>(2:45 pm-3:00 pm break)</i> <b>Session 10:</b> Recent Advances in Silicon Based Terahertz Solutions	1:00 pm-5:05 pm <i>(2:45 pm-3:00 pm break)</i> <b>Session 11:</b> Analog Sensor Interfaces
5:30 pm-7:30 pm <b>Welcome Reception</b> <i>River Terrace and Patio</i>				



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Tuesday, April 25, 2023				
Salon A	Salon B	Salon C	Salon E	Salon F
8:00 am-10:00 am <b>Session 12:</b> Forum: Recent Progress in LDOs and Voltage, Current, and Timing References	8:00 am-10:00 am <b>Session 13:</b> Forum: Emerging Electrical and Optical Devices for Biomedical Applications	8:00 am-10:50 am (9:45 am -10:00 am break) <b>Session 14:</b> Heterogenous SoCs for Next-Gen Compute Applications	8:00 am-11:40 am (9:45 am -10:00 am break) <b>Session 15:</b> Frequency Generation, Clocking and Power Transfer	8:00 am-11:15 am (9:45 am -10:00 am break) <b>Session 16:</b> ADCs with Noise Shaping
10:10 am-11:30 am <b>Session 17:</b> Analog Techniques	10:10 am-11:50 am A-SSCC Best Student Papers	12:00 pm-1:30 pm <b>Session 18:</b> Luncheon Keynote Session <i>Salon D</i>		
1:45 pm-4:35 pm (3:30 pm-3:45 pm break) <b>Session 19:</b> Timing Circuits	1:45 pm-5:00 pm (3:30 pm-3:45 pm break) <b>Session 20:</b> Machine Learning	1:45 pm-4:35 pm (3:30 pm-3:45 pm break) <b>Session 21:</b> Mixed-Signal Foundational IPs for Emerging Systems	1:45 pm-3:15 pm <b>Session 22:</b> Panel: It's 2023. Where are our self-driving cars?	1:45 pm-5:25 pm (3:30 pm-3:45 pm break) <b>Session 23:</b> Advances in Low-power, High-performance Sensor Interfaces
4:30 pm-6:00 pm <b>IEEE SSCS Young Professionals and Women in Circuits Mentoring Event</b> <i>Salon D</i>				
5:30 pm-7:30 pm <b>CICC Conference Reception</b> <i>River Terrace and Patio</i>				
Wednesday, April 26, 2023				
8:00 am-8:50 am <b>Session 24:</b> Keynote Session <i>Salon C</i>				
8:50 am-9:00 am BREAK				
Salon A	Salon B	Salon C	Salon E	Salon F
9:00 am-10:30 am <b>Session 25:</b> Panel: Improving ASIC Productivity	9:00 am-11:00 am <b>Session 26:</b> Forum: Standardizing Chiptlet Design	9:00 am-10:45 am <b>Session 27:</b> Advanced Techniques for Wireline Communications	9:00 am-10:45 am <b>Session 28:</b> mm-Wave Transceiver and Front-end Building Blocks for Radar and Communication	9:00 am-10:45 am <b>Session 29:</b> Gigasample-Rate Data Converters
10:45 am-1:00 pm BREAK				
1:00 pm-2:20 pm <b>Session 30:</b> Hardware Security	1:00 pm-2:30 pm <b>Session 31:</b> Panel: Where is the balance between circuit and system-level innovation in our solid-state circuit conference?	1:00 pm-2:30 pm <b>Session 32:</b> Panel: CHIPS Act and Future of Semiconductor Innovation	1:00 pm-2:45 pm <b>Session 33:</b> Energy Harvesting and Wireless/Isolated Power Converters	1:00 pm-2:15 pm <b>Session 34:</b> SAR-based Gigasample-rate ADCs
2:45 pm-3:00 pm BREAK				
3:00 pm-4:00 pm <b>Best Paper Poster Session &amp; Closing/Awards Ceremony</b> <i>Salon C</i>				