



2023 Program-at-a-Glance

Conference will take place in San Antonio, Texas, USA, Central Daylight Time (CDT)

Sunday, April 23, 2023				
<i>*All Sunday sessions are included with conference registration*</i>				
Salon A	Salon B	Salon C	Salon F	
9:00 am-4:45 pm (12:15 pm-1:30 pm break) Educational Session 1	9:00 am-4:45 pm (12:15 pm-1:30 pm break) Educational Session 2	9:00 am-4:45 pm (12:15 pm-1:30 pm break) Educational Session 3	9:00 am-4:45 pm (12:15 pm-1:30 pm break) Educational Session 4	
Monday, April 24, 2023				
8:00 am-8:20 am Welcome and Opening Remarks <i>Salon C</i>				
8:20 am-9:10 am Session 1: Keynote Session <i>Salon C</i>				
9:10 am-9:30 am BREAK				
Salon A	Salon B	Salon C	Salon E	Salon F
9:30 am-11:40 am Session 2: Low-power Digital Circuits	9:30 am-11:30 am Session 3: Forum: Ultra High-Speed Data Converters	9:30 am-11:40 am Session 4: Gate Drivers and GaN ICs	9:30 am-11:40 am Session 5: Low Power Quantum Computing & Wireless Transceivers	9:30 am-11:40 am Session 6: Architectures for Advancing Computing
11:40 am-1:00 pm BREAK				
1:00 pm-4:40 pm (2:45 pm-3:00 pm break) Session 7: Compute in Memory and Ising Machines	1:00 pm-4:15 pm (2:45 pm-3:00 pm break) Session 8: Data Converter Design Techniques	1:00 pm-5:05 pm (2:45 pm-3:00 pm break) Session 9: DC-DC Converters	1:00 pm-4:15 pm (2:45 pm-3:00 pm break) Session 10: Recent Advances in Silicon Based Terahertz Solutions	1:00 pm-5:05 pm (2:45 pm-3:00 pm break) Session 11: Analog Sensor Interfaces
5:30 pm-7:30 pm Welcome Reception <i>River Terrace and Patio</i>				



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Tuesday, April 24, 2023				
Salon A	Salon B	Salon C	Salon E	Salon F
8:00 am-10:00 am Session 12: Forum: Recent Progress in LDOs and Voltage, Current, and Timing References	8:00 am-10:00 am Session 13: Forum: Emerging Electrical and Optical Devices for Biomedical Applications	8:00 am-10:50 am (9:45 am -10:00 am break) Session 14: Heterogenous SoCs for Next-Gen Compute Applications	8:00 am-11:40 am (9:45 am -10:00 am break) Session 15: Frequency Generation, Clocking and Power Transfer	8:00 am-11:15 am (9:45 am -10:00 am break) Session 16: ADCs with Noise Shaping
10:10 am-11:30 am Session 17: Analog Techniques	10:10 am-11:50 am A-SSCC Best Student Papers	12:00 pm-1:30 pm Session 18: Luncheon Keynote Session <i>Salon D</i>		
1:45 pm-4:35 pm (3:30 pm-3:45 pm break) Session 19: Timing Circuits	1:45 pm-5:00 pm (3:30 pm-3:45 pm break) Session 20: Machine Learning	1:45 pm-4:35 pm (3:30 pm-3:45 pm break) Session 21: Mixed-Signal Foundational IPs for Emerging Systems	1:45 pm-3:15 pm Session 22: Panel: It's 2023. Where are our self-driving cars?	1:45 pm-5:25 pm (3:30 pm-3:45 pm break) Session 23: Advances in Low-power, High-performance Sensor Interfaces
5:30 pm-7:30 pm CICC Conference Reception <i>River Terrace and Patio</i>				
Wednesday, April 26, 2023				
8:00 am-8:50 am Session 24: Keynote Session <i>Salon C</i>				
8:50 am-9:00 am BREAK				
Salon A	Salon B	Salon C	Salon E	Salon F
9:00 am-10:30 am Session 25: Panel: Improving ASIC Productivity	9:00 am-11:00 am Session 26: Forum: Standardizing Chiplet Design	9:00 am-10:45 am Session 27: Advanced Techniques for Wireline Communications	9:00 am-10:45 am Session 28: mm-Wave Transceiver and Front-end Building Blocks for Radar and Communication	9:00 am-10:45 am Session 29: Gigasample-Rate Data Converters
10:45 am-1:00 pm BREAK				
1:00 pm-2:20 pm Session 30: Hardware Security	1:00 pm-2:30 pm Session 31: Panel: Where is the balance between circuit and system-level innovation in our solid-state circuit conference?	1:00 pm-2:30 pm Session 32: Panel: CHIPS Act and Future of Semiconductor Innovation	1:00 pm-2:45 pm Session 33: Energy Harvesting and Wireless/Isolated Power Converters	1:00 pm-2:15 pm Session 34: SAR-based Gigasample-rate ADCs
2:45 pm-3:00 pm BREAK				
3:00 pm-4:00 pm Best Paper Poster Session & Closing/Awards Ceremony <i>Salon C</i>				