

Best Poster Paper

Paper M-18

A 10 GHz Frequency-Drift Temperature Compensated LC VCO with Fast-Settling Low-Noise Voltage Regulator in 0.13 μm CMOS, *Hiroshi Akima, Aleksander Dec, Timothy Merkin, Ken Suyama, Epoch Microelectronics, Inc.*



Hiroshi Akima received the B.S and M.S degrees in electronic engineering from Tokyo Denki University, Tokyo, Japan, in 2002 and 2004, respectively.

In 2004, he joined Epoch Microelectronics Inc., NY, as a Design Engineer. His research interests are in the design of analog and RF integrated circuits in CMOS technologies, with particular focus on analog RF front-end, such as LNAs, RFVGAs, Attenuators, Mixers, Polyphase Filters, VCOs, and Frequency dividers.