

## Best Student Paper

### *Paper 3-3*

**Statistical Modeling and Post Manufacturing Configuration for Scaled Analog CMOS**, *Gokce Keskin, Jonathan Proesel, Larry Pileggi, Carnegie Mellon University*



Gokce Keskin graduated with a B.S. degree in Electrical and Electronics Engineering from Bilkent University, Ankara, Turkey in 2003. He completed his M.S. degree in Electrical and Computer Engineering at Carnegie Mellon University, Pittsburgh, PA in 2005, working on power supply noise in integrated circuits. He was a PCI Express circuit design intern at Intel Corp., Chandler, AZ during 2006. He completed the Ph.D. degree at Carnegie Mellon University in 2010 developing analog circuit design methodologies in the presence of high process variability.

He joined Intel Corp., Hillsboro, OR in December 2010. He is currently working on high speed signaling for next generation I/O interfaces.