

### Best Invited Paper:

13-5. "Challenges in the Design of Cognitive Radios", Behzad Razavi, University of California, Los Angeles, CA

### Behzad Razavi



Behzad Razavi is an award-winning researcher, teacher, and author. He holds a PhD from Stanford University and has been Professor of Electrical Engineering at University of California, Los Angeles, since 1996. His current research includes RF and wireless design, broadband data communication circuits, phase-locking phenomena, and data converter design.

Professor Razavi has received numerous awards for his research and teaching, including the Beatrice Winner Award for Editorial Excellence at the 1994 International Solid-State Circuits Conference (ISSCC), the best paper award at the 1994 European Solid-State Circuits Conference, the best panel award at the 1995 and 1997 ISSCC, the TRW Innovative Teaching Award in 1997, and the best paper award at the IEEE Custom Integrated Circuits Conference in 1998. He was the co-recipient, with his students, of both the Jack Kilby Outstanding Student Paper Award and the Beatrice Winner Award for Editorial Excellence at the 2001 ISSCC. He received the Lockheed Martin Excellence in Teaching Award in 2006 and the UCLA Faculty Senate Teaching Award in 2007. He was also recognized as one of the top 10 authors in the 50-year history of ISSCC.

Professor Razavi is an IEEE Distinguished Lecturer and a Fellow of IEEE. He has published more than 150 papers and a number of books in the area of analog and RF design. He is the author of *Principles of Data Conversion System Design*, *RF Microelectronics* (translated to Chinese, Japanese, and Korean), *Design of Analog CMOS Integrated Circuits* (translated to Chinese and Japanese), *Design of Integrated Circuits for Optical Communications*, and *Fundamentals of Microelectronics* (translated to Korean). He is also the editor of *Monolithic Phase-Locked Loops and Clock Recovery Circuits*, and *Phase-Locking in High-Performance Systems*.