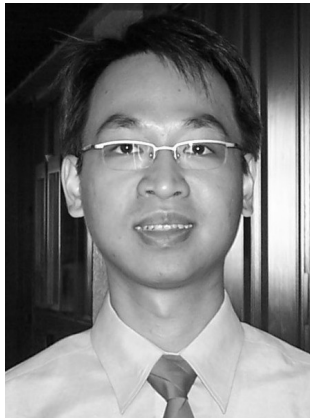


Best Student Paper:

10-4. "An Area- and Power-efficient Monolithic Buck Converter with Fast Transient Response", Ying Wu, Sam Tsui and Philip Mok, Hong Kong University of Science and Technology, Clear Water Bay, Hong Kong

Ying WU



Ying Wu received the Bachelor of Engineering degree in electrical engineering from the University of Hong Kong in 2001, Master of Science degree in electrical engineering from the University of Southern California in 2003, and Ph. D degree in electrical and electronic engineering at Hong Kong University of Science and Technology (HKUST) in 2009.

In 2000, he was with Motorola, Hong Kong, to work on a PDA evaluation board as a summer intern. His research interests include the design of data converter, switched-capacitor circuits, linear regulator, and switch mode power supply.